SPECIFICATION

产品说明书

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修改记录

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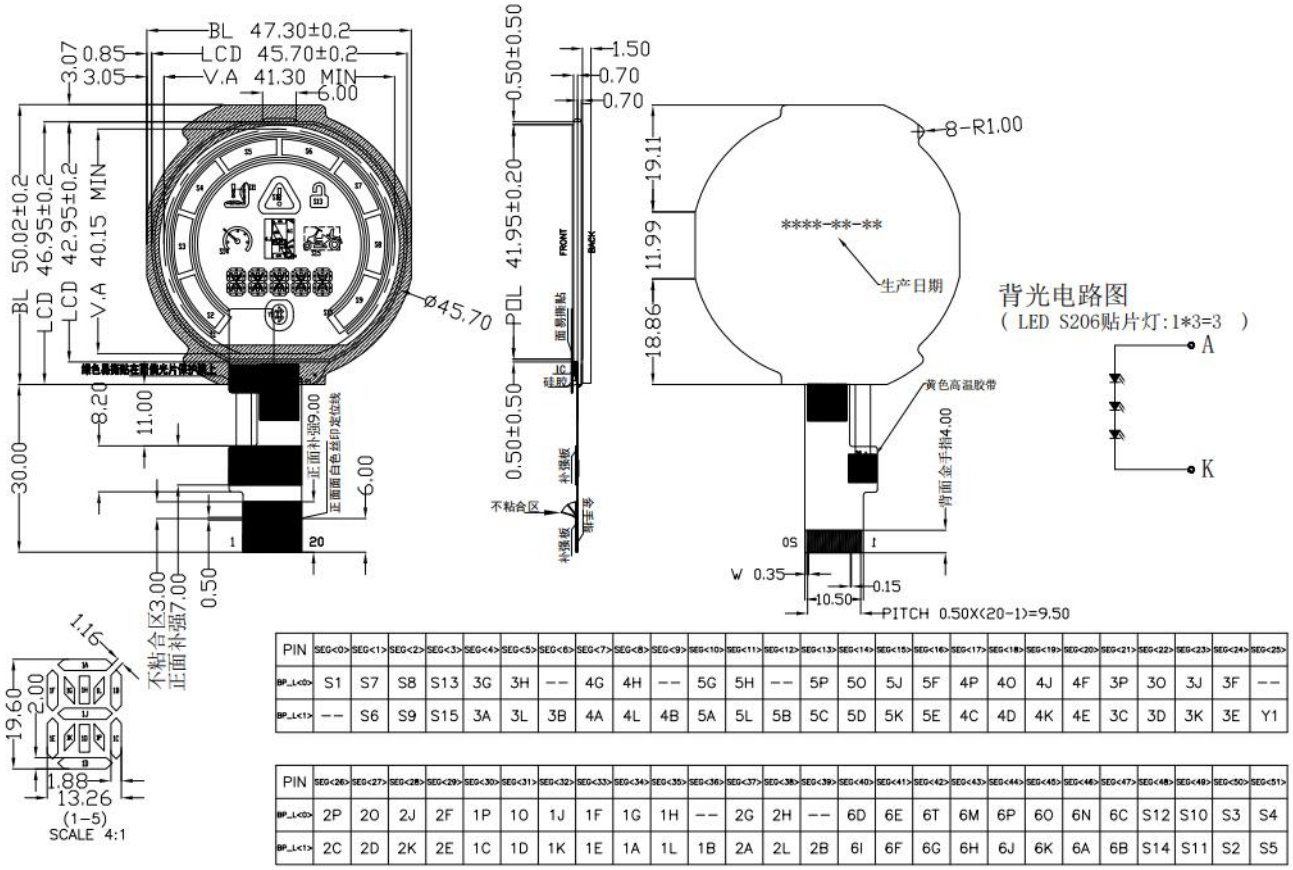
■ GENERAL INFORMATION

主要特征描述

ITEM OF GRNERAL INFORMATION 项目	CONTENTS 内容	UNIT 单位
LCD Type 液晶显示类型	BTN/TRANSMISSIVE/NEGATIVE	/
Recommended Viewing Direction 模块推荐使用方向	6:00	O’ Clock
Module area (W*H*T) 模块外围尺寸（宽*高*厚）	47.30×50.02×3.30	mm
Viewing area (W*H) 可视区域（宽*高）	41.30×40.15	mm
Active area (W*H) 有效区域（宽*高）	40.30×36.10	mm
Number of Dots 点阵	-	/
Pixel pitch (W*H) 像素间隙(宽*高)	-	mm
Driver IC 驱动集成电路	UC1672C	/
Interface Type 接口类型	I2C	/
Input voltage 输入电压	3.3V	V
Module Power consumption 模块功耗	TBD	mW
Backlight Type 背光类型	LED （white）	/

■ EXTERANL DIMENSIONS

外形尺寸



PIN	SEG<0>	SEG<1>	SEG<2>	SEG<3>	SEG<4>	SEG<5>	SEG<6>	SEG<7>	SEG<8>	SEG<9>	SEG<10>	SEG<11>	SEG<12>	SEG<13>	SEG<14>	SEG<15>	SEG<16>	SEG<17>	SEG<18>	SEG<19>	SEG<20>	SEG<21>	SEG<22>	SEG<23>	SEG<24>	SEG<25>
BP_L<0>	S1	S7	S8	S13	3G	3H	--	4G	4H	--	5G	5H	--	5P	5O	5J	5F	4P	4O	4J	4F	3P	3O	3J	3F	--
BP_L<1>	--	S6	S9	S15	3A	3L	3B	4A	4L	4B	5A	5L	5B	5C	5D	5K	5E	4C	4D	4K	4E	3C	3D	3K	3E	Y1

PIN	SEG<26>	SEG<27>	SEG<28>	SEG<29>	SEG<30>	SEG<31>	SEG<32>	SEG<33>	SEG<34>	SEG<35>	SEG<36>	SEG<37>	SEG<38>	SEG<39>	SEG<40>	SEG<41>	SEG<42>	SEG<43>	SEG<44>	SEG<45>	SEG<46>	SEG<47>	SEG<48>	SEG<49>	SEG<50>	SEG<51>
BP_L<0>	2P	2O	2J	2F	1P	1O	1J	1F	1G	1H	--	2G	2H	--	6D	6E	6T	6M	6P	6O	6N	6C	S12	S10	S3	S4
BP_L<1>	2C	2D	2K	2E	1C	1D	1K	1E	1A	1L	1B	2A	2L	2B	6I	6F	6G	6H	6J	6K	6A	6B	S14	S11	S2	S5

■ INTERFACE DESCRIPTION

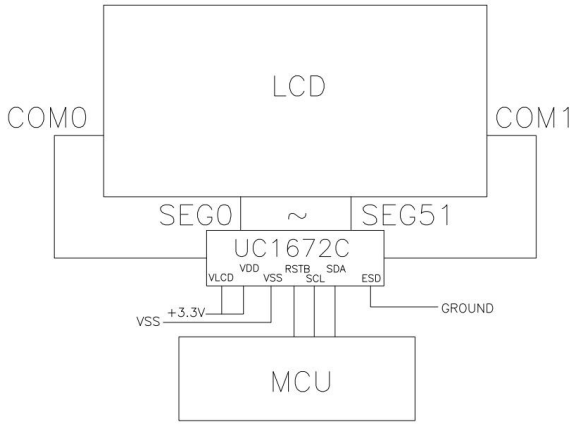
接口定义描述

Interface NO. 接口序号	Symbol 符号	I/O or connect to 输入/出或连接到	Description 描述
1~2	BLA	P	Back Light Power.
3~4	BLK	P	Back Light Ground.
5/20	ESD	P	Ground. Electrostatic discharge PIN.
6	SCL	I	I2C clock signal.
7	SDA	I	I2C data input.
8	RSTB	I	IC Reset Pin,(low active).
9~11	VDD	P	Connected to 3.3V power
12~16	VSS	P	Ground.
17~19	VLCD	P	LCD supply voltage.

■ REFERENCE APPLICATION CIRCUIT

参考应用电路

1、 BLOCK DIAGRAM 模块框图



■ TIMING CHARACTERISTICS

时序特征(For I2C)

MPU write timing:

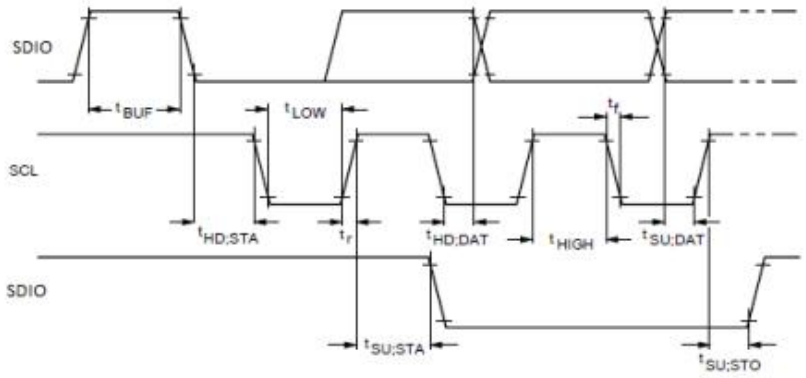


Figure: I²C Series Interface Characteristics

3V ≤ V_{DD} < 5.5V, Ta= −30 to +85°C

Symbol	Signal	Description	Condition	Min.	Typ.	Max.	Unit
I2C bus timing							
fSCL	SCL	SCL clock frequency		–	–	400	kHz
tHIGH	SCL	HIGH period of the SCL clock		0.6	–	–	uS
tLOW	SCL	LOW period of the SCL clock		1.3	–	–	uS
SDIO pin							
tSU/DAT	SDIO	Data set-up time		180	–	–	nS
tHD;DAT	SDIO	Data hold time		0	–	–	nS
SCL and SDIO pins							
Tbuf	SCL, SDIO	Bus free time between STOP & START		1.3	–	–	uS
Tsu;sto	SCL, SDIO	Set-up time from STOP		0.6	–	–	uS
Thd;sta	SCL, SDIO	Hold time (repeated) START		0.6	–	–	uS
Tsu;sta	SCL, SDIO	Set-up time for a repeated START		0.6	–	–	uS
Tr	SCL, SDIO	Rise time of both SDIO and SCL signals		–	–	0.3	uS
Tf	SCL, SDIO	Fall time of both SDIO and SCL signals		–	–	0.3	uS
Cb	SCL, SDIO	Capacitive load for each bus line		–	–	400	pF
Tw(spike)	SCL, SDIO	Spike pulse width	On bus	–	–	50	nS

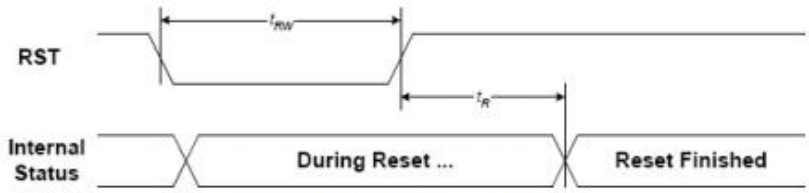


FIGURE 15: Reset Characteristics

($3V \leqslant V_{DD} < 5.5V$, $Ta = -30$ to $+85^{\circ}C$)

Symbol	Signal	Description	Condition	Min.	Max.	Units
t_{RW}	RSTB	Reset low pulse width		1	–	mS
t_R	RSTB Internal Status	Reset to Internal Status pulse delay		3	–	mS

Please refer UC1672C DATASHEES

具体请详细参考 UC1672C 规格书

■ COMMAND DESCRIPTION

命令描述

COMMAND TABLE

The following is a list of host commands supported by UC1672c

C/D: 0: Control, 1: Data D7-D0: #: Useful Data bits –: Don't Care,.

When DA2/DA1/DA0 matches the PIN A2/A1/A0 of the IC, data will be written, otherwise data will be ignored. Except for code 0x0 and code 0x2, all devices will be executed at the same time.

#	Command	C/D	D7	D6	D5	D4	D3	D2	D1	D0	Action	Default
1	Software Reset	0	0	1	0	1	0	0	0	0	Software Reset	50H
2	Select Device	0	0	1	0	1	0	0	0	1	DEV[2:0]	51H
		1	0	0	0	0	0	#	#	#		00H
3	Select Frame-Rate & Blink	0	0	1	0	1	0	0	1	0	BF[1:0],A,FR[2:0]	52H
		1	0	0	#	#	#	#	#	#		02H
4	Pump Control	0	0	1	0	1	0	0	1	1	ES,CS[1:0],CPF[1:0], VLDMS[1:0]	53H
		1	0	#	#	#	#	#	#	#		0CH
5	Pump Voltage Set	0	0	1	0	1	0	1	0	0	VSET[5:0]	54H
		1	0	0	#	#	#	#	#	#		32H
6	Display Mode	0	1	1	1	1	1	0	I	O	APO,AXV,I.O,M[1:0] B,E	55H
		1	#	#	#	#	#	#	#	#		00H
7	Select RAM Address/Data	0	0	1	0	1	0	1	1	0	Select RAM Address	56H
		1	0	#	#	#	#	#	#	#		00H
		1	#	#	#	#	#	#	#	#	RAM_DAT[7:0]	00H
8	Get Status	0	0	1	0	1	1	1	1	0	Get Status	5EH
		1	-	-	#	#	#	#	-	#		N/A

COMMAND DESCRIPTION

(1) SOFTWARE RESET

Action	C/D	D7	D6	D5	D4	D3	D2	D1	D0
Software Reset	0	0	1	0	1	0	0	0	0

This command is Software reset; all register will reset to default value. Please wait for at least 3mS after Software Reset before issuing any commands.

(2) SELECT DEVICE

Action	C/D	D7	D6	D5	D4	D3	D2	D1	D0
Select Device	0	0	1	0	1	0	0	0	1
	1	0	0	0	0	0	DEV[2:0]		

DEV[2:0]: 3-bit binary value of 0 to 7, transferred to the sub-address counter to define one of 8 hardware sub-addresses.

(3) SELECT FRAME-RATE & BLINK

Action	C/D	D7	D6	D5	D4	D3	D2	D1	D0
Select Frame rate & Blink	0	0	1	0	1	0	0	1	0
	1	0	0	BF[1:0]		A	FR[2:0]		

BF[1:0]: blink frequency selection

00b: off 01b: blink mode1 10b: blink mode 2 11b: blink mode 3

See the Blink Frequency table for detailed description about each blink mode.

A: blink mode selection

0b: normal blinking. Normal blinking can only be selected when multiplex drive mode=1:3 or 1:4.

1b: blinking by alternating display RAM banks

FR[2:0]: SEG/COM Frame Rate Selection

000b: 60Hz 100b: 140Hz
001b: 80Hz 101b: 160Hz
010b: 100Hz 110b: 180Hz
011b: 120Hz 111b: 200Hz

(4) PUMP CONTROL

Action	C/D	D7	D6	D5	D4	D3	D2	D1	D0
Pump Control	0	0	1	0	1	0	0	1	1
	1	0	ES	CS[1:0]		CPF[1:0]		VLCDMS[1:0]	

ES: EVENT Pin setting

0b: Disable, EVENT PIN always output '0'.

1b: Enable, when any one of BYTE_ERR/RST_DET is '1', then EVENT PIN will output '1'

CS[1:0]: CLK Pin setting

00b: TCON will use internal OSC clock.

01b: TCON will use internal OSC clock and also output this clock to CLK PIN.

10b: TCON will use external clock from CLK PIN.

11b: Reserved.

CPF[1:0]: PUMP1 frequency divider setting

00b: 1MHz 10b: 2MHz 01b: 4MHz **11b: 8MHz**

VLCDMS[1:0]: VLCD mode setting

00b: PUMP disabled; external supply of VLCD.

01b: PUMP enabled, internal VLCD generation.

10b,11b: Direct mode enabled, VDD is internal connected to VLCD, PUMP disabled.

(5) PUMP VOLTAGE SET

Action	C/D	D7	D6	D5	D4	D3	D2	D1	D0
Pump Voltage Set	0	0	1	0	1	0	1	0	0
	1	0	0	VSET[5:0]					

VSET[5:0]: VLCD Voltage Setting, VLCD = 2.5 + VSET * 0.05(**default 5V**).

(6) DISPLAY MODE

Action	C/D	D7	D6	D5	D4	D3	D2	D1	D0
Display Mode	0	0	1	0	1	0	1	0	1
	1	APO	AXV	I	O	M[1:0]		B	E

APO: All Pixel On control

0b: All pixel on disable

1b: All pixel on enable

AXV: All Pixel On Inverse

0b: Normal

1b: Inverse

I: Input bank selection, that is to select the storage of arriving display data

0b: RAM bit 0 (RAM bits 0 and 1)

1b: RAM bit 2 (RAM bits 2 and 3)

O: Output bank selection, that is to select the retrieval of LCD display data

0b: RAM bit 0 (RAM bits 0 and 1)

1b: RAM bit 2 (RAM bits 2 and 3)

M[1:0]: LCD drive mode selection

00b: 1:4 multiplex; 4 backplanes

01b: static; 1 backplane

10b: 1:2 multiplex; 2 backplanes

11b: 1:3 multiplex; 3 backplanes

B: LCD bias configuration

0b: 1/3 bias

1b: 1/2 bias

E: Display Enable

0b: display off

1b: display on

(7) SELECT RAM ADDRESS/DATA

Action	C/D	D7	D6	D5	D4	D3	D2	D1	D0
Select RAM Address/Data	0	0	1	0	1	0	1	1	0
	1	0	DP[6:0]						
	1	RAM_DAT[7:0]							
	1	.....							
	1	RAM_DAT[7:0]							

DP[6:0]: 7-bit binary value of 0 to 119, transferred to the data pointer to define one of 120 display RAM addresses.

Value range: 0000000b~1110111b, that is 0~119 in decimal**(default 00H)**

RAM_DAT[7:0]: Display RAM data

(8) GET STATUS

Action	C/D	D7	D6	D5	D4	D3	D2	D1	D0
Get Status	0	0	1	0	1	1	1	1	0
	1	-	-	ERR_SDOX	ERR_CMD	ERR_BYTE	RST_DET	-	CP_OK

ERR_SDOX: SDO Output conflict

ERR_CMD: Interface command error.

1b: Command write is not equal 0x50 ~ 0x5F. (Read clear to 0)

ERR_BYTE: The last bit of interface transmission does not byte boundary. (Read will clear to 0)

RST_DET: Reset Detection. If this is 1, then it means that PIN reset has occurred,

and if it is 0, then it means that no PIN reset has occurred. Read will clean to 0.

CP_OK: VLCD Voltage is Ready.

Please refer UC1672C DATASHEES

具体请详细参考 UC1672C 规格书

■ MAXIMUM ABSOLUTE RATINGS

极限参数

Parameter of absolute maximum ratings 参数	Symbol 符号	Min 最小值	Max 最大值	Unit 单位
Supply voltage for logic 逻辑电压	VDD	-0.3	5.5	V
Supply Voltage for LCD LCD 电压	VLCD	-0.3	5.5	V
Input Voltage 输入电压	Vin	-0.4	VDD+0.3	V
Supply Current for Backlight 背光电流	If(Ta=25° C)	-	15	mA
Operating temperature 操作温度	TOP	-30	80	° C
Storage temperature 储存温度	TST	-30	80	° C

Note: Absolute maximum ratings means the product can withstand short-term, NOT more than 500 hours. If the product is a long time to withstand these conditions, the life time would be shorter.

备注:极限条件仅指产品能短暂承受的范围,不可超过 500 小时。如果产品长时间在极限条件,将有损产品的使用寿命。

■ ELECTRICAL CHARACTERISTICS

模块电气特性

DC CHARACTERISTICS

直流特性

Parameter of DC characteristics 参数	Symbol 符号	Min 最小值	Typ 典型值	Max 最大值	Unit 单位
Supply voltage for logic 逻辑电压	VDD	3.0	3.3	3.6	V
Supply Voltage for LCD LCD 电压	VLCD	3.0	3.3	3.6	V
Input voltage 'H' level 输入高电平	VIH	0.8VDD	-	VDD	V
Input voltage 'L' level 输入低电平	VIL	0	-	0.2VDD	V
Output voltage 'H' level 输出高电平	VOH	0.8VDD	-	VDD	V
Output voltage 'L' level 输出低电平	VOL	0	-	0.2VDD	V
Supply Current 模块电流	Idd	-	-	-	mA

■ BACKLIGHT CHARACTERISTICS

背光电气特性

Item of backlight characteristics 项目(Yellow-Green Blacklight)	Symbol 符号	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位	Condition 条件
Forward voltage 正向电压	Vf	7.8	8.4	9.0	V	If=15mA
Number of LED 灯数	-	-	3	-	Piece	-
Blacklight Colour 背光颜色	-	-	white	-	-	-

■ ELECTRO-OPTICAL CHARACTERISTICS

光电参数

Item of backlight characteristics 项目	Symbol 符号	Condition 条件	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位	Remark 注释	Note 备注
Response time 响应时间	Tr	θ=0° ø=0° Ta=25℃	-	162	-	ms	FIG 1	4
	Tf			126				
Contrast ratio 对比度	Cr		3	4	-	-	FIG 2	1
Luminance uniformity 均匀度	δ WHITE		87	89	-	%		3
Surface Luminance 表面亮度	Lv		810	899	-	cd/m2		2
Viewing angle range 视角范围	θ	ø = 90°	60	80	-	deg	FIG 3	6
		ø = 270°	60	80	-	deg		
		ø = 0°	60	90	-	deg		
		ø = 180°	60	90	-	deg		

Note1. Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG 2.

$$\text{Contrast Ratio(CR)} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, ……)}}{\text{Average Surface Luminance with all black pixels (P1, P2, ……)}}$$

备注 1. 对比度是由以下公式计算所得。详见 FIG 2.

$$\text{对比度} = \frac{\text{显示白色画面时平均表面亮度(P1,P2,…)}}{\text{显示黑色画面时平均表面亮度(P1,P2,…)}}$$

Note2. Surface luminance is the LCD surface luminance with all white pixels. For more information see FIG 2.

Lv = Average Surface Luminance with all white pixels(P1, P2,……)

备注 2. 表面亮度是在显示白色画面时，测试的亮度值，详见 FIG 2.

Lv = 平均的表面亮度(P1, P2,……)

Note3. The uniformity in surface luminance(δWHITE) is determined by measuring luminance at each test position, and then dividing the maximum luminance of all white pixels by minimum luminance of all white pixels. For more information see FIG 2.

$$\delta\text{WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels(P1, P2,……)}}{\text{Maximum Surface Luminance with all white pixels(P1, P2,……)}}$$

备注 3. 均匀度是在显示白色画面时，测试 P1 到 P9 的亮度，然后再用 9 个点亮度的最小值除以最大值。详见 FIG 2.

$$\text{均匀度} = \frac{\text{白色画面下表面亮度最小值 (P1, P2,……)}}{\text{_白色画面下表面高度最大值 (P1,P2,……)}}$$

Note4. Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1.

备注 4. 响应时间是 Tr（上升时间）与 Tf（下降时间）的和；Tr 指显示白色画面转为显示黑色画面需要时间，Tf 指显示黑色画面转为显示白色画面需要时间。详见 FIG 1.

Note5. CIE(x, y)chromaticity is the Center point value. For more information see FIG 2.

备注 5. 选择中心点分别测试 x,y 值。详见 FIG 2.

Note6. Viewing angle is the angle at which the contrast ratio is greater than a specific value. For TFT module, the specific value of conrast ratio is 10; For monochrome and color stn module, the specific value of conrast ratio is 2. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

备注 6. 视角指对比度大于等于一个特定值时的可视范围，对 TFT 屏，对比度特定值为 10，对黑白屏，CSTN 屏，对比度特定值为 2。视角由横轴（x 轴），竖轴（y 轴）同 Z 轴（垂直于 LCD 表面）之间的夹角来定义。详见 FIG 3.

Note7. For Viewing angle and response time testing, the testing data is base on Autronic-Melchers’s ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on CS-2000 photo detector.

备注 7. 视角和响应时间，测试数据基于 Autronic-Melchers’s ConoScope. 系列。而对比度，表面亮度，均匀度，CIE 坐标，测试数据基于 CS-2000 photo detector。

Note8. For TN type TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle

备注 8. TN 型 TFT 全透产品，在视角方向会发生灰度反转。

FIG 1. The definition of Response Time

响应时间

响应时间定义

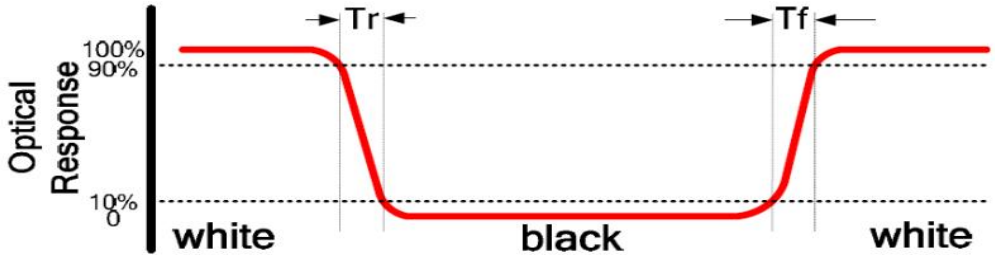


FIG 2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE(x, y) chromaticity.

对比度、表面亮度、均匀度、CIE 坐标测试方法

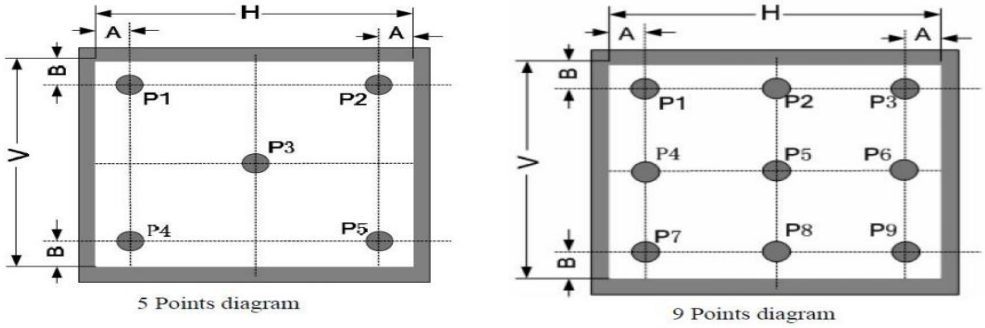


Fig2 Note1: For TFT Module Test point:9 points(as 9 Points diagram)

A : H/6 B : V/6

H,V : Active Area(AA) size

Measurement instrument:CS-2000;Light spot size ø=5mm, 350mm distance from the LCD surface to detector lens.

Fig2 Note2: For non-TFT Module and Dot-Matrix type Module

2.1 If the minimum side size is bigger than 20 mm, the testing method is the same as TFT module.

2.2 If the minimum side size is less than 20 mm, then testing 5 point datas (as 5 Points diagram), Both A and B are 5 mm.

2.3 Measurement instrument: CS-2000 is priority selected to measure. Light spot size ø=5mm, 350mm distance from the LCD surface to detector lens.

2.4 Measurement instrument : ConoScope will be selelected to measure If CS-2000 cannot meet the measurement requirement. Light spot size ø =0.2-2.0mm. About 2-3mm distance from the LCD surface to detector lens, but suggest to confirm the best distance on focusing the picture to be clearest when actually measuring.

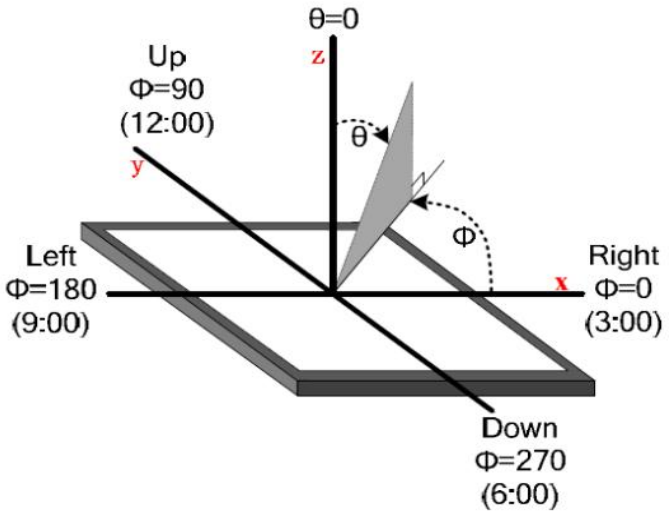
Fig2 Note3: For non-TFT Module and non-Dot-Matrix type Module

The test point is defined by the fact size and shape of module, but the center point and four edges should be selected.

3.1 Measurement instrument: CS-2000 is priority selected to measure. Light spot size Æ=5mm, 350mm distance from the LCD surface to detector lens.

3.2 Measurement instrument : ConoScope will be selelected to measure If CS-2000 cannot meet the measurement requirement. Light spot size Æ=0.2-2.0mm. About 2-3mm distance from the LCD surface to detector lens, but suggest to confirm the best distance on focusing the picture to be clearest when actually measuring.

FIG.3. The definition of viewing angle
视角定义



■ RELIABILITY TEST CONDITIONS
可靠性试验条件

No. 序号	Test Item 试验项目	Test Condition 试验条件	Inspection after test 判断标准
1	High Temperature Storage 高温存放	80 ±2℃/500 hours	Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 试验结束后,已测试的 LCD 样品必须在室内正常温湿度环境下放置 2~4 个小时以上才能进行功能和外观检查，样品不允许有以下缺陷： 1.Air bubble in the LCD;模块中有气泡； 2.Sealleak; 封口松脱； 3.Non-display;不显示； 4.missing segments; 漏笔 5.Glass crack;玻璃破碎； 6.Current Idd is twice higher than initial value.电流 Idd 大于初时值的 2 倍
2	Low Temperature Storage 低温存放	-30 ±2℃/500 hours	
3	High Temperature Operating 高温操作	80±2℃/500 hours	
4	Low Temperature Operating 低温操作	-30±2℃/500 hours	
5	Temperature Cycle storage 冷热循环存放	-30±2℃~25~80±2℃×10cycles (30min.) (5min.) (30min.)	
6	Damp proof Test operating 防潮试验操作	60℃±5℃×90%RH/500 hours	
7	Vibration Test 振荡试验	Frequency：10Hz~55Hz~10Hz Amplitude： 1.5mm, X, Y, Z direction for total 3hours (Packing condition)	
8	Dropping test 跌落试验	Drop to the ground from 1m height, one time,every side of carton.(Packing condition)	
9	ESD test 静电试验	Voltage:±4KV R: 330Ω C: 150pF Air discharge, 10time	
Remark:注意：			

1. The test samples should be applied to only one test item.
   每个被测试的模块只能用于其中的一个测试项目。
2. Sample size for each test item is 3~5pcs.
   每个测试项目的样品数量为 3～5 片.
3. For Damp Proof Test, Pure water(Resistance＞10MΩ) should be used.
   对于防潮试验，试验箱的用水必须是电阻大于 10M 欧姆的纯水。
4. In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
   如果由静电引起产品故障,当放置一段时间后能够恢复正常，则不视为产品缺陷。
5. EL evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
   带 EL 片的可靠性测试在高温高湿条件下,荧光粉会发生自然化学反应而产生黑点或瑕疵,因此不在高温高湿条件测试范围内.
6. Failure Judgment Criterion:Basic Specification,Electrical Characteristic,Mechanical Characteristic, Optical Characteristic.
   故障判断标准:基本规格,电气特性,机械特性,光电特性

■ Sampling Plan and Reference Standards
抽样及检测标准
Based on GB/T 2828.1---2003/ISO2859-1:1999：

Inspection items	Sampling Rate	AQL Assessment
Appearance	Normally checking the sampling plan one time and performing general inspection level II	MA=0.4 MI=1.0
Function	Normally checking the sampling plan one time and performing general inspection level II	MA=0.4 MI=1.0
Size	N=3	C=0

GB/T 2828.1---2003/ISO2859-1:1999 checking the counting sampling procedure and sampling table.
GB/T 1619.96: Test methods for TN LCD parts.
GB/T 12848.91: General Specification for STN LCD parts.
GB2421-89: Basic Environmental Test Procedures for Electrical and Electronic Products.
IPC-A-610C: The acceptance condition for electrician assembled.